Express Mail Label No.: EL 844 515 165 US

ATTORNEY'S DOCKET NO.: S1022/8827

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Cesare CLEMENTI, Gabriella GHIDINI, and Carlo RIVA

Serial No.: Filing Date:

Unassigned Herewith

For:

PROCESS FOR FORMING AN INTEGRATED CIRCUIT COMPRISING NON-VOLATILE MEMORY CELLS AND SIDE TRANSISTORS OF AT

LEAST TWO DIFFERENT TYPES, AND CORRESPONDING IC

Examiner:

Unassigned

Art Unit:

Unassigned

Box Patent Application Commissioner for Patents Washington, D.C. 20231

Sir:

STATEMENT FILED PURSUANT TO THE DUTY OF DISCLOSURE UNDER 37 CFR §§1.56, 1.97 AND 1.98

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicant requests consideration of this Information Disclosure Statement.

PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed within three months of the filing date of a National Application. No fee or certification is required.

PART II: Information Cited

The Applicant hereby makes of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicant hereby makes the following additional information of record in the above-identified application.

The examiner's attention is hereby drawn to the attached European search report.

PART III: Remarks

Documents cited on the attached form PTO-1449 (modified) are enclosed unless otherwise indicated on the attached form PTO-1449 (modified). It is respectfully requested that:

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Attorney's Express Mail Label No.: EL 844 515 165 US Docket No.: S1022/8827 -2-

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;

- 2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
- 3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicant makes no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicant makes no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,

Cesare CLEMENTI, Gabriella GHIDINI, and Carlo RIVA

Bv:`

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Docket No. S1022/8827 Dated: January 14, 2002

FORM PTO-1449/A and B (Modified)					PPLICATION NO.: Unassigned	ATTY DOCKET NO. C1002/0007			
FORM F10-1445/A and B (Modified)					APPLICATION NO.: Unassigned ATTY. DOCKET NO.: S1022/8				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					LING DATE: Herewith		10		
					PPLICANT: Cesare CLEMEN				
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		·		U.S	PATENT DOCUMENTS			JC.	
Examiner's Initials#	Cite No.	U.S. Patent Document		ent	Name of Patentee or Applicant of Cited Document		Date of Publication or of issue of Cited Document MM-DD-YYY		e
		Number		Kind Code					
		4,471,373			Shinji Shimizu, et al.		09/11/84		
		4,780,424			Mark A. Holler et al.		10/25/88		
		5,104,819			Philip E. Freiberger et al.		04/14/92		
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Examiner's Initials#	Cite No.	Foreign Patent Document		nent	Name of Patentee or Applicant of Cited		Date of Publication of Translat	Translati	ion
		Office/ Country	Number	Kind Code	Document (not necessary)		Cited Document MM-DD-YYYY	()	
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					N PATENT LITERATURE DOCUMEN				
Examiner's Initials#	Cite No							Translatio (Y/N)	
		IEEE IEDM Technical Digest 93, 1993 pp 321-324, Hsing-Huang Tsend et al., "Thin CVD Stacked Gate Dielectric For ULSI Technology"							
						<u></u>			

EXAMINER DATE CONSIDERED

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.